Low Phase Noise

PE33241 Integer-N PLL

- Normalized phase noise floor figure of merit: -230 dBc/Hz
- Low power consumption of 80 µA @ 2.8 V
- Dual modulus prescaler: 10/11 to 5 GHz and 5/6 to 4 GHz

Typical Phase Noise for a STeP5 PLL
VDD = 2.8V, Temp = 25°C, fvco = 4 GHz, fc = 50 MHz, Loop Bandwidth = 500 kHz