Product Description

The PE4270 is a high-isolation Switch designed for CATV applications, covering a broad frequency range from 1 to 3000 MHz. This single-supply SPST switch offers a single-pin CMOS control interface with industry leading CTB performance. It also provides low insertion loss, high isolation and extremely low bias requirements while operating on a single 3-volt supply. In a typical CATV application, the PE4270 provides for a cost effective and manufacturable solution vs. mechanical relays.

The PE4270 is manufactured on Peregrine’s UltraCMOS™ process, a patented variation of silicon-on-insulator (SOI) technology on a sapphire substrate, offering the performance of GaAs with the economy and integration of conventional CMOS.

Figure 1. Functional Diagram

Peregrine Specification 71/0010

![Functional Diagram](image)

Table 1. Electrical Specifications @ +25 °C (ZS = ZL = 75 Ω)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Condition</th>
<th>Minimum</th>
<th>Typical</th>
<th>Maximum</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating Frequency</td>
<td>1 – 50 MHz 1000 MHz</td>
<td>1</td>
<td>0.50</td>
<td>3000</td>
<td>MHz</td>
</tr>
<tr>
<td>Insertion Loss</td>
<td>1 – 50 MHz 1000 MHz</td>
<td>85</td>
<td>90</td>
<td>0.65</td>
<td>dB</td>
</tr>
<tr>
<td>Isolation</td>
<td>1 – 50 MHz 1000 MHz</td>
<td>60</td>
<td>63</td>
<td>0.85</td>
<td>dB</td>
</tr>
<tr>
<td>Return Loss</td>
<td>5 - 1000 MHz, VCTRL = 3.0V</td>
<td>15</td>
<td>16</td>
<td></td>
<td>dB</td>
</tr>
<tr>
<td>1 dB Compression</td>
<td>1000 MHz</td>
<td>28</td>
<td>30</td>
<td></td>
<td>dBm</td>
</tr>
<tr>
<td>CTB / CSO</td>
<td>77 &amp; 110 channels; PO = 44 dBmV</td>
<td>-90</td>
<td></td>
<td></td>
<td>dBc</td>
</tr>
<tr>
<td>Input IP2</td>
<td>1000 MHz</td>
<td>80</td>
<td></td>
<td></td>
<td>dBm</td>
</tr>
<tr>
<td>Input IP3</td>
<td>1000 MHz</td>
<td>50</td>
<td></td>
<td></td>
<td>dBm</td>
</tr>
<tr>
<td>Video Feedthrough</td>
<td></td>
<td></td>
<td>15</td>
<td></td>
<td>mVpp</td>
</tr>
<tr>
<td>Switching Time</td>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td>μS</td>
</tr>
</tbody>
</table>

Notes:
1. Device linearity will begin to degrade below 1 MHz.
2. Measured in a 50 Ω system.
3. Measured with a 1 ns risetime, 0/3 V pulse and 500 MHz bandwidth.
4. Note Absolute Maximum ratings in Table 3.
Electrostatic Discharge (ESD) Precautions

When handling this UltraCMOS™ device, observe the same precautions that you would use with other ESD-sensitive devices. Although this device contains circuitry to protect it from damage due to ESD, precautions should be taken to avoid exceeding the rating specified in Table 3.

Latch-Up Avoidance

Unlike conventional CMOS devices, UltraCMOS™ devices are immune to latch-up.

Device Description

The PE4270 high isolation SPST CATV Switch is designed to support CATV applications such as premise disconnect of a CATV signal path. This function is typically performed by bulky and expensive mechanical relays. The high isolation characteristics, high compression point, and integrated 75-ohm terminations make the PE4270 an ideal, cost effective and manufacturable product of choice.

The control logic input pin (CTRL) is typically driven by a 3-volt CMOS logic level signal, and has a threshold of 50% of VDD. For flexibility to support systems that have 5-volt control logic drivers, the control logic input has been designed to handle a 5-volt logic HIGH signal. (A minimal current will be sourced out of the VDD pin when the control logic input voltage level exceeds VDD.)
Typical Performance Data @ -40 °C to 85 °C (Unless Otherwise Noted)
(75 Ω impedance except as indicated)

**Figure 4. Insertion Loss - RF1 to RF2**

**Figure 5. 1dB Compression & 3rd Order Intercept Point (T = 25°C)**

**Figure 6. Isolation - RF1 to RF2**
Typical Performance Data @ -40 °C to 85 °C (Unless Otherwise Noted)
(75-ohm impedance)

Figure 7. RF1 Return Loss (Switch = ON)

Figure 8. RF1 Return Loss (Switch = OFF)

Figure 9. RF2 Return Loss (Switch = ON)

Figure 10. RF2 Return Loss (Switch = OFF)
**Evaluation Kit**

The **PE4270 EK** board was designed to ease customer evaluation of Peregrine’s high performance SPST CATV MOSFET switch. RF1 is connected through a 75 Ω transmission line via the top left F connector, J1. RF2 is connected through a 75 Ω transmission line via the top right F connector, J2. A 75 Ω through transmission line is available via F connectors J3 and J4. This transmission line can be used to estimate the loss of the PCB over the environmental conditions being evaluated. VDD is supplied via J6-2, while the control logic voltage is supplied via J5-2. It is the responsibility of the customer to determine proper supply decoupling for their design application. It has been observed that by removing C1 and C2 from the evaluation board has not shown to degrade RF performance.

The board is constructed of a two metal layer FR4 material with a total thickness of 0.031”. The bottom layer provides ground for the RF transmission lines. The transmission lines were designed using a coplanar waveguide model with trace width of 0.021”, trace gaps of 0.030”, dielectric thickness of 0.028”, metal thickness of 0.0021” and εr of 4.6. Note that the predominate mode for these transmission lines is coplanar waveguide with a ground plane.

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**Figure 11. Evaluation Board Layouts**
Peregrine Specification 101/0167 (with F connectors)

![Evaluation Board Layouts](image)

**Figure 12. Evaluation Board Schematic**
Peregrine Specification 102/0224

![Evaluation Board Schematic](image)
Figure 13. Package Drawing

6-lead DFN

NOTE: The exposed solder pad (on the bottom of the package) is not electrically connected to any other pin (isolated).

Figure 14. Marking Specifications

YYWW = Date Code (last two digits of year and work week)
ZZZZZ = Last five digits of Lot Number
### Figure 15. Tape and Reel Specifications

6-lead DFN

#### Table 6. Dimensions

<table>
<thead>
<tr>
<th>Dimension</th>
<th>DFN 3x3 mm</th>
</tr>
</thead>
<tbody>
<tr>
<td>Ao</td>
<td>3.23 ± 0.1</td>
</tr>
<tr>
<td>Bo</td>
<td>3.17 ± 0.1</td>
</tr>
<tr>
<td>Ko</td>
<td>1.37 ± 0.1</td>
</tr>
<tr>
<td>P</td>
<td>4 ± 0.1</td>
</tr>
<tr>
<td>W</td>
<td>8 ±0.3,-0.1</td>
</tr>
<tr>
<td>T</td>
<td>0.254 ± 0.02</td>
</tr>
<tr>
<td>R7 Quantity</td>
<td>3000</td>
</tr>
<tr>
<td>R13 Quantity</td>
<td>N.A.</td>
</tr>
</tbody>
</table>

Note: R7 = 7 inch Lock Reel, R13 = 13 inch Lock Reel

#### Table 7. Ordering Information

<table>
<thead>
<tr>
<th>Order Code</th>
<th>Part Marking</th>
<th>Description</th>
<th>Package</th>
<th>Shipping Method</th>
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</thead>
<tbody>
<tr>
<td>4270-51</td>
<td>4270</td>
<td>PE4270G-06DFN 3x3mm-12800F</td>
<td>Green 6-lead 3x3 mm DFN</td>
<td>Tape or loose</td>
</tr>
<tr>
<td>4270-52</td>
<td>4270</td>
<td>PE4270G-06DFN 3x3mm-3000C</td>
<td>Green 6-lead 3x3 mm DFN</td>
<td>3000 units / T&amp;R</td>
</tr>
<tr>
<td>4270-00</td>
<td>PE4270-EK</td>
<td>PE4270-06DFN 3x3mm-EK</td>
<td>Evaluation Kit</td>
<td>1 / Box</td>
</tr>
</tbody>
</table>

**NOTES:**
1. SPROCKET HOLE PITCH CUMULATIVE TOLERANCE ±0.2
2. CAMBER IN COMPLIANCE WITH EIA 481
3. POCKET POSITION RELATIVE TO SPROCKET HOLE MEASURED AS TRUE POSITION OF POCKET, NOT POCKET HOLE
Data Sheet Identification

Advance Information
The product is in a formative or design stage. The data sheet contains design target specifications for product development. Specifications and features may change in any manner without notice.

Preliminary Specification
The data sheet contains preliminary data. Additional data may be added at a later date. Peregrine reserves the right to change specifications at any time without notice in order to supply the best possible product.

Product Specification
The data sheet contains final data. In the event Peregrine decides to change the specifications, Peregrine will notify customers of the intended changes by issuing a CNF (Customer Notification Form).

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