

6

5

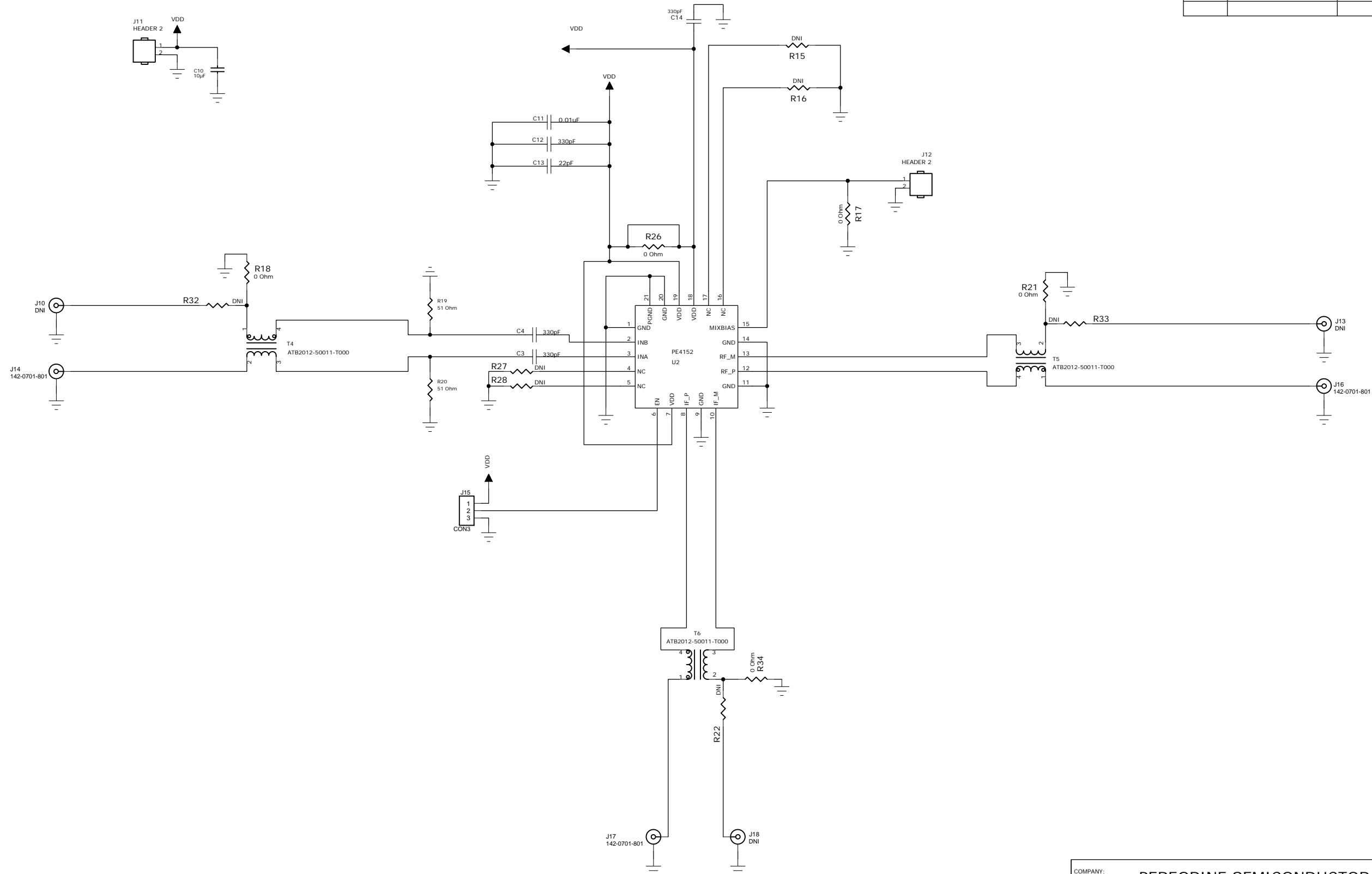
4

3

2

1

| REVISION RECORD |                                |           |          |
|-----------------|--------------------------------|-----------|----------|
| LTR             | ECO NO:                        | APPROVED: | DATE:    |
| 02              | UPDATED XFMR P/N               | ZOYA KHAN | 03-23-15 |
| 04              | REMOVED SHT 1 SECTION          | G. WU     | 8-24-15  |
| 05              | ADD JUMPER TO BOM J15 PINS 2&3 | G. WU     | 10-06-15 |



NOTES:  
 1. FOR PCB USE PRT-60631-03  
 2. ADD JUMPER BETWEEN PINS 2 & 3 OF J15.  
 3. CAUTION:  
 CONTAINS PARTS AND ASSEMBLIES SUSCEPTIBLE  
 TO DAMAGE BY ELECTROSTATIC DISCHARGE (ESD)

|                       |                    |
|-----------------------|--------------------|
| DRAWN:<br>R.Marroquin | DATED:<br>10-06-15 |
| CHECKED:<br>G.Wu      | DATED:<br>10-06-15 |
| QUALITY CONTROL:      | DATED:             |
| RELEASED:             | DATED:             |

|                                     |            |                          |               |
|-------------------------------------|------------|--------------------------|---------------|
| COMPANY:<br>PEREGRINE SEMICONDUCTOR |            |                          |               |
| TITLE:<br>SCH, PE4152 EK            |            |                          |               |
| CODE:                               | SIZE:<br>C | DRAWING NO:<br>DOC-64072 | REV:<br>05    |
| SCALE:                              |            |                          | SHEET: 1 OF 1 |

